a. Figures 1-3 below show examples of a subproject that includes assets on multiple circuit segments. Text boxes in the figures describe the circumstances under which this type of project would include assets on multiple circuit segments.
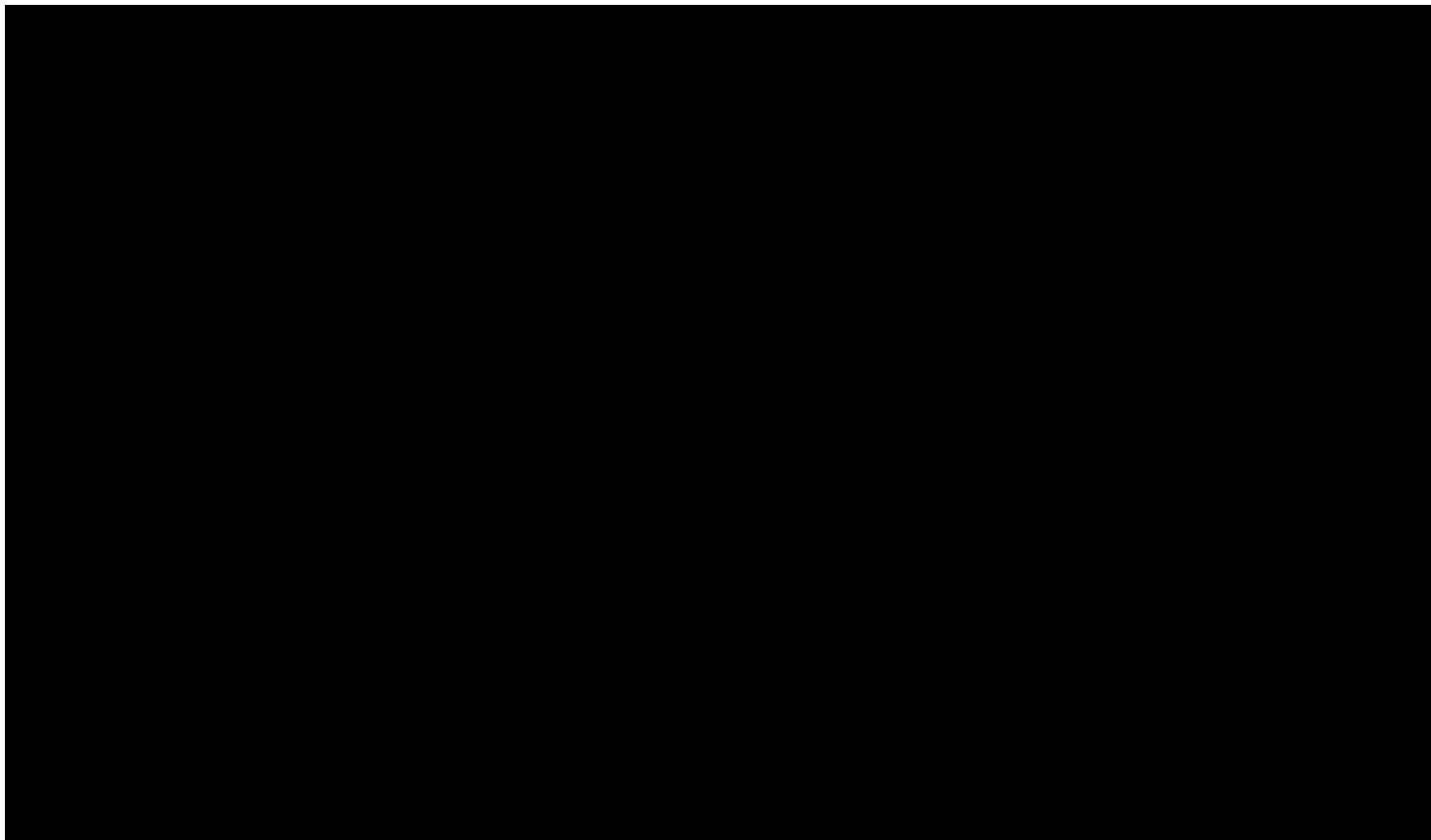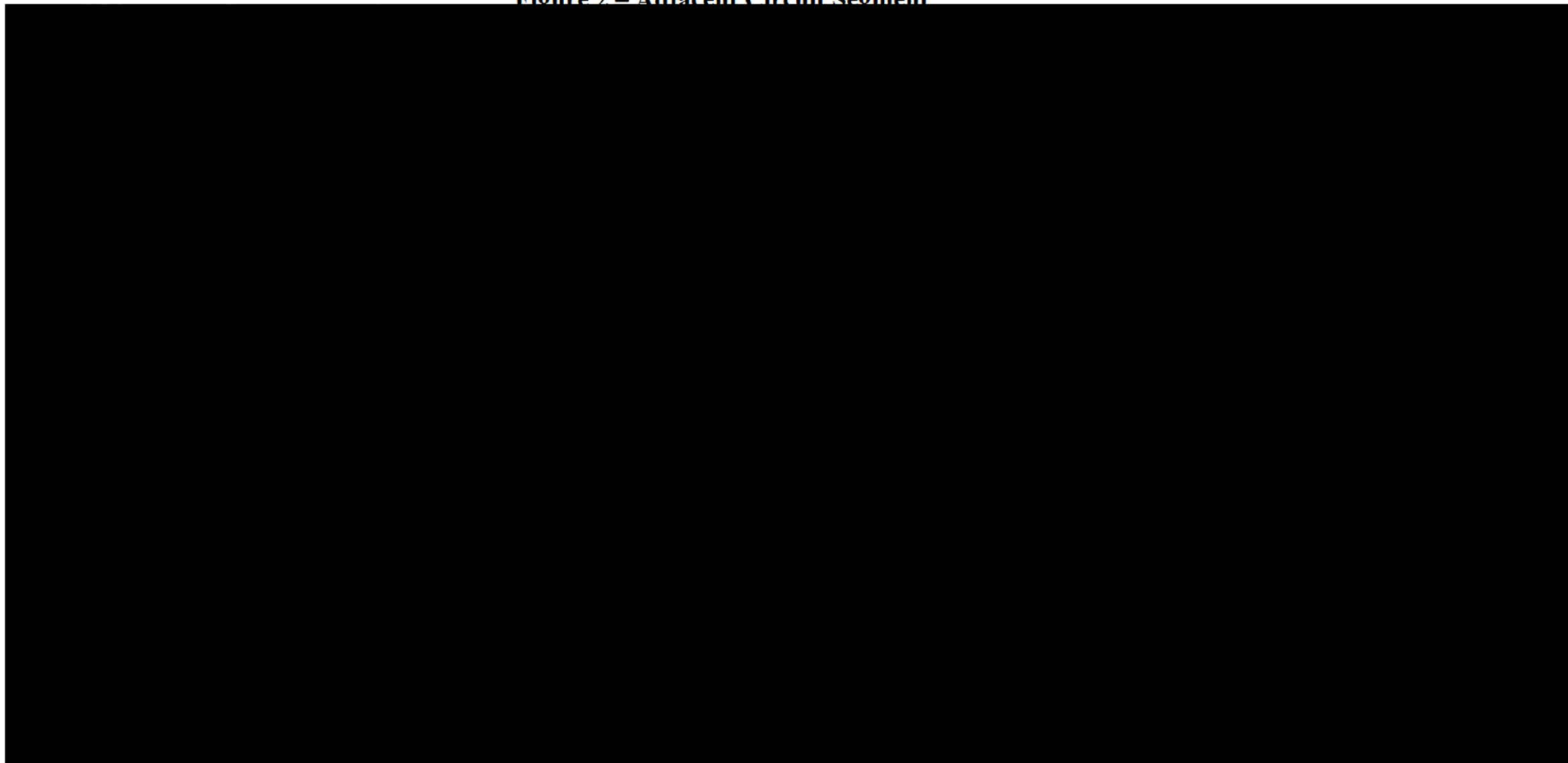
Figure 1– Double Circuit

Figure 2 – Adjacent Circuit Segment

Figure 3 – Segment Border